Pulse Instruments

CUSTOMER QUESTIONNAIRE

INFRARED DEVICE
TEST SYSTEM CONFIGURATION

To configure an appropriate test system for your application there are key areas requiring definition. We will use the answers provided to the following questions as a guide to providing a test system configured to meet your present and future testing applications. All answers provided to the following questions will be held in strict confidence by Pulse Instruments.

CUSTOMER:_____ **DATE:**_____

I) TEST APPLICATIONS

a) Focal Plane Array (Multiplexed Device):

i) Size of the Array/Arrays (e.g. 128 x 128; 512 x 512 etc.):_____

_____

ii) Device package type: LCC_____ Other _____

iii) Number of pins (e.g. 68, 84 etc.):_____

iv) Number device of signal outputs per package (e.g. 1, 2, etc.):_____

v) Output signal offset voltage: _____ Volts

vi) Wavelengths of Interest:

(1) Short <2.5 um _____

(2) Medium 2.5 - 5.5 um _____

(3) Long 8.0 - 14.0 um _____

(4) XLong > 14 um _____

vii) Clocking Frequency (Frequency of Reset Clock):_____

II) DEVICE TEST ENVIRONMENTAL CONDITIONS

a) Optical Stimulus Source

i) Blackbody: Extended Source: _____ Point Source:_____

ii) Temperature Range: 50 - 1000 C:_____ 50 - 1200 C:_____

iii) Blackbody Cavity size: 0.50 inches: _____ 1.00 inches:_____

iv) Apertures:

(1) Smallest aperture required for testing;_____ diameter

(2) Largest aperture required for testing;_____ diameter

v) Filters required: Yes _____ No _____ Quantity:_____

vi) Chopper: Number of Teeth: _____ Frequency:_____

b) Dewar Environment

i) Device Temperature Range:_____

ii) Device Temperature Stability:_____
iii) Temperature Resolution:_____

III) BASIC SYSTEM HARDWARE REQUIREMENTS

a) System Bias Supplies

i) Number of DC bias supplies per device _____
ii) Maximum voltage range: +/-_____V
iii) Maximum current required per bias: _____mA
iv) Do all bias supplies need the above capability? Yes _____ No _____

b) System Clock Drivers

i) Number of clock inputs per device _____
ii) Maximum voltage levels +/- _____ V
iii) Maximum clock amplitude (swing) _____ V
iv) Clocking frequency required _____
v) Minimum (fastest) rise/fall times required _____
vi) Maximum (slowest) rise/fall times required _____

c) Data Acquisition

i) Number of outputs: _____
ii) Analog or digital outputs? _____
iii) Pixel Data Rate (in Hertz):_____
iv) A/D channels, type required:
(1) Number of bits: _____
(2) Fastest required digitization rate (Hz): _____
(3) Slowest required digitization rate (Hz): _____
v) Gain Required (x100, x1000 etc.): _____

Thank you for answering the above questions. The above answers will be of significant help in configuring a system to meet your requirements.

Person to contact, if additional questions or clarification is required:

Name:_____
Email: _____
Product Webpage:_____
Telephone:_____
Fax:_____